

Application No.: 10/055,545

Docket No: JCLA6997-R

In The Claims:

Claims have amended as follows:

Claim 1. (currently amended) A multi-domain vertical alignment liquid crystal display, comprising at least:

a first substrate, having a plurality of thin-film transistors, a plurality of protrusions and a plurality of pixel electrodes thereon, wherein the pixel electrodes formed over the protrusions have a plurality of slits, whereby the protrusions and the slits produce a multi-domain mechanism, and the first substrate further includes a planarized dielectric layer on the protrusions and the slits;

a second substrate, having none of slit or protrusion for use to produce additional electric field; and

a liquid crystal layer disposed between the first substrate and the second substrate.

Claim 2. (original) The multi-domain vertical alignment liquid crystal display according to claim 1, wherein the first and second substrates include glass substrates.

Claim 3. (original) The multi-domain vertical alignment liquid crystal display according to claim 1, wherein the first substrate includes a thin-film transistor array substrate.

Claim 4. (original) The multi-domain vertical alignment liquid crystal display according to claim 1, wherein the second substrate has a plurality of black matrices and color filters.

Application No.: 10/055,545

Docket No: JCLA6997-R

Claim 5. (original) The multi-domain vertical alignment liquid crystal display according to claim 1, wherein extension directions of the protrusions and the slits are parallel to each other.

Claim 6. (original) The multi-domain vertical alignment liquid crystal display according to claim 1, wherein extension directions of the protrusions and the slits are not parallel to each other.

Claim 7. (previously amended) The multi-domain vertical alignment liquid crystal display according to claim 1, wherein the protrusions and the slits are alternately arranged.

Claim 8. (original) The multi-domain vertical alignment array liquid crystal display according to claim 1, wherein the pixel electrodes on the protrusions are exposed while the dielectric layer is planarized.

Claim 9. (original) The multi-domain vertical alignment array liquid crystal display according to claim 1, wherein the dielectric layer covers the pixel electrodes on the protrusions with a thickness thinner than that of the dielectric layer in other positions.

Claim 10. (currently twice amended) A thin-film transistor array substrate, comprising:
a substrate;

Application No.: 10/055,545

Docket No: JCLA6997-R

a plurality of thin-film transistors formed on the substrate to provide an electric field,
wherein protrusions are also formed on the substrate;

a plurality of pixel electrodes covering the protrusions and having a plurality of slits
arranged with the protrusions to form multiple domains, wherein the slits and the protrusions are
only disposed over the substrate but not on an opposite substrate against the substrate; and

a dielectric layer, covering the pixel electrodes and the slits, the dielectric layer having a
planarized surface.

Claim 11. (previously amended) The thin-film transistor array substrate according to
claim 10, wherein the thin-film transistor array substrate includes a glass substrate.

Claim 12. (previously amended) The thin-film transistor array substrate according to
claim 10, wherein extension directions of the protrusions and the slits are parallel to each other.

Claim 13. (previously amended) The thin-film transistor array substrate according to
claim 10, wherein extension directions of the protrusions and the slits are not parallel to each
other.

Claim 14. (previously amended) The thin-film transistor array substrate according to
claim 10, wherein protrusions and the slits are alternately arranged with each other.

Application No.: 10/055,545

Docket No: JCLA6997-R

Claim 15. (previously amended) The thin-film transistor array substrate according to claim 10, wherein the pixel electrodes on the protrusions are exposed.

Claim 16. (previously amended) The thin-film transistor array substrate according to claim 10, wherein the dielectric layer covers the pixel electrodes on the protrusions with a thickness thinner than the dielectric layer in other positions.